

Fig. 1

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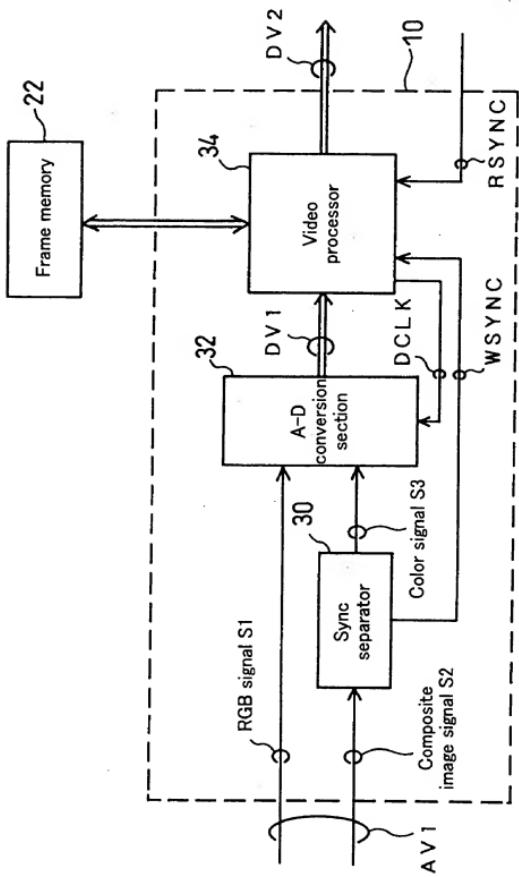


Fig. 2

000080" T2225960

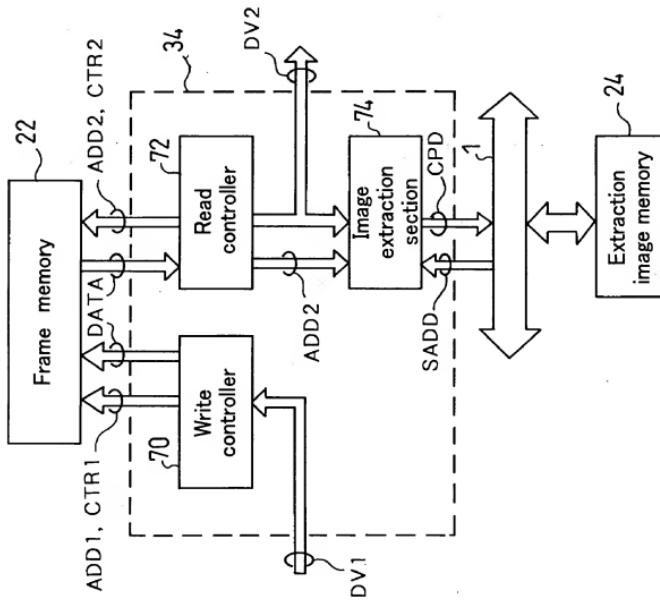


Fig. 3

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SHEET 4 OF 26

Fig. 4 (a)

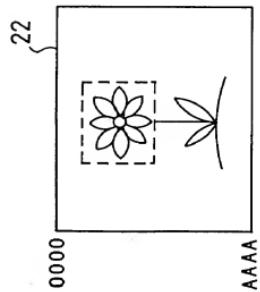


Fig. 4 (b)

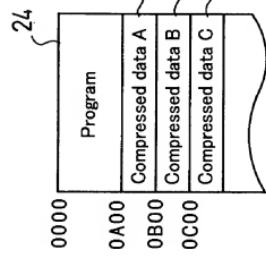


Fig. 4 (c)

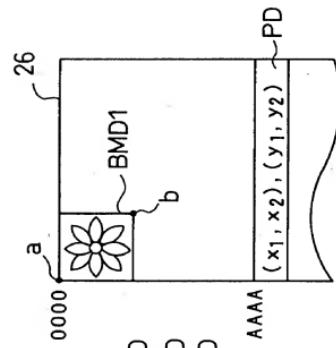


Fig. 4 (d)

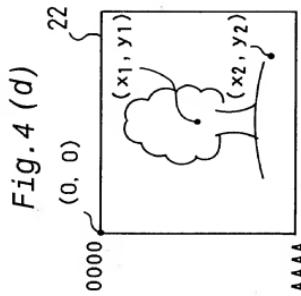
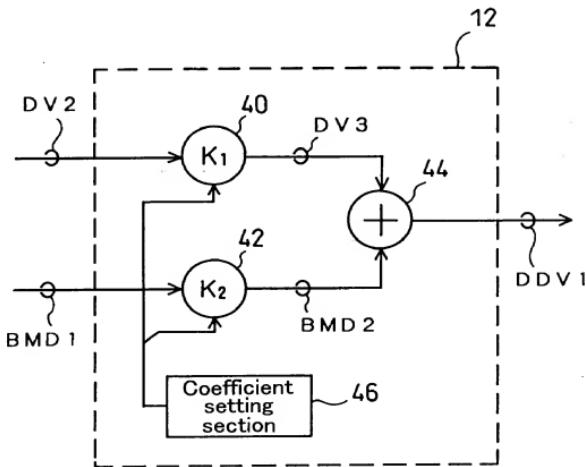


Fig. 5



09632224 00308000

Fig. 6 (a)

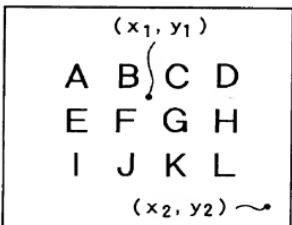


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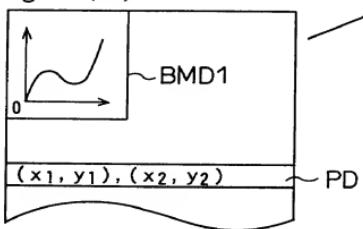


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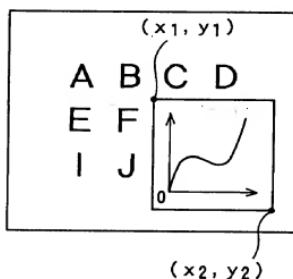


Fig. 7

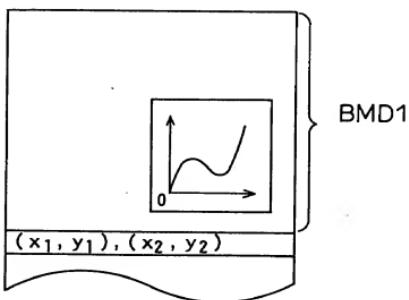


Fig. 8 (a)

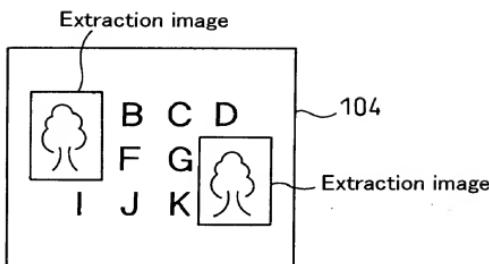


Fig. 8 (b)

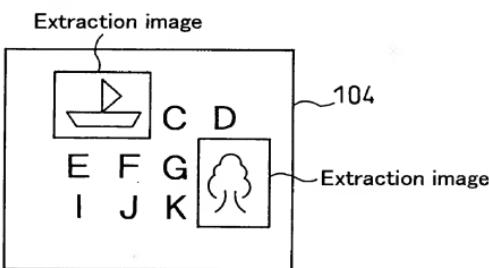
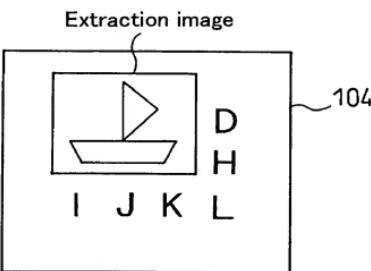


Fig. 8 (c)



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Fig. 9

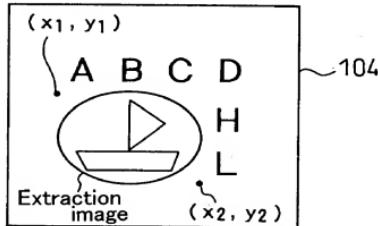


Fig. 10

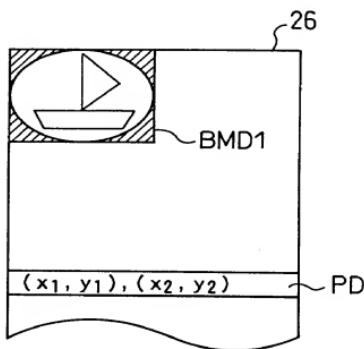


Fig. 11(a)

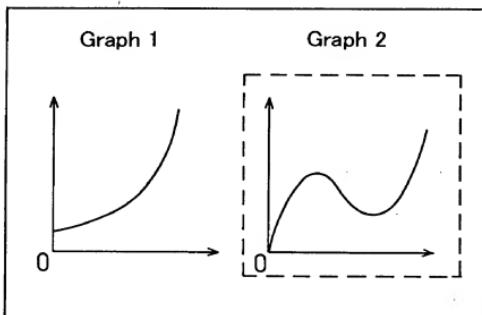
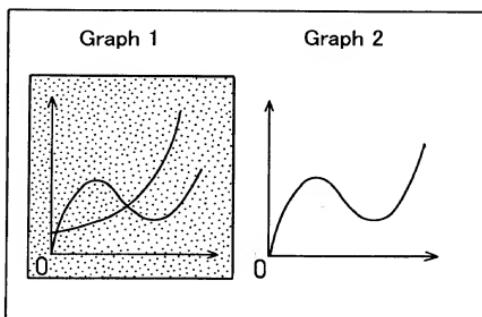
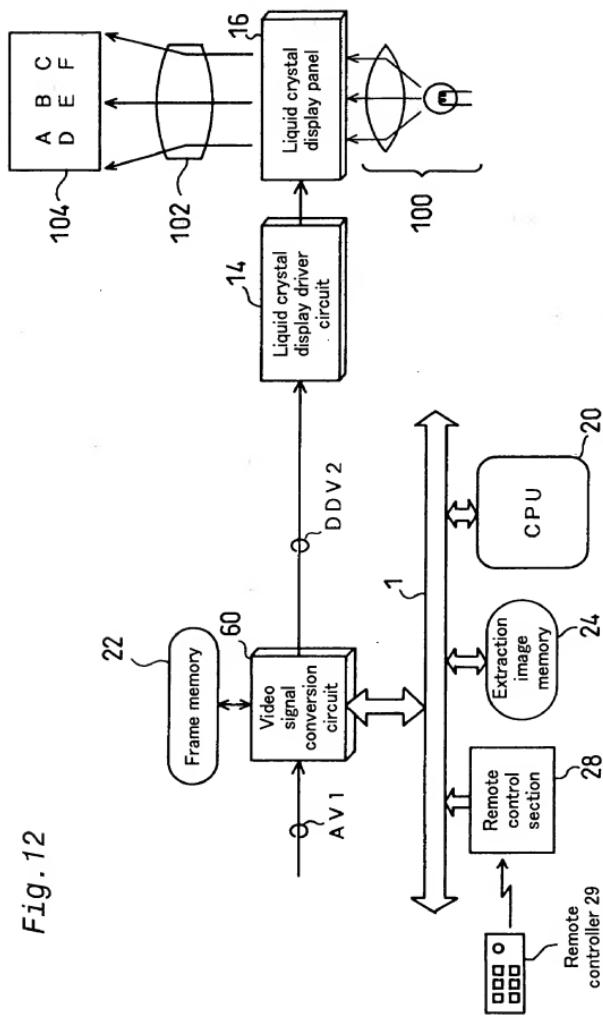


Fig. 11(b)



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Fig. 12



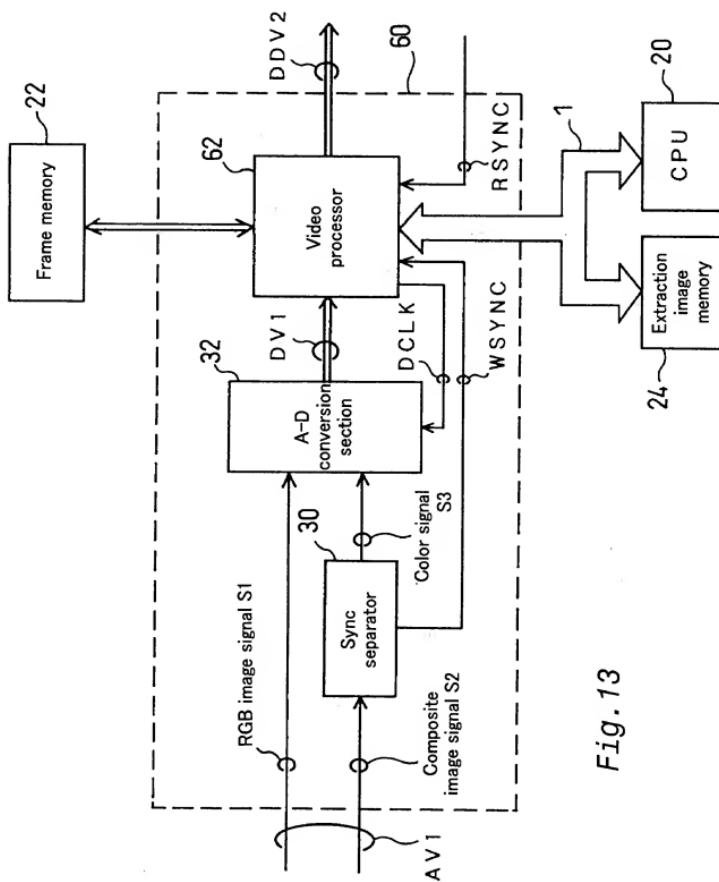


Fig. 13

Fig. 14

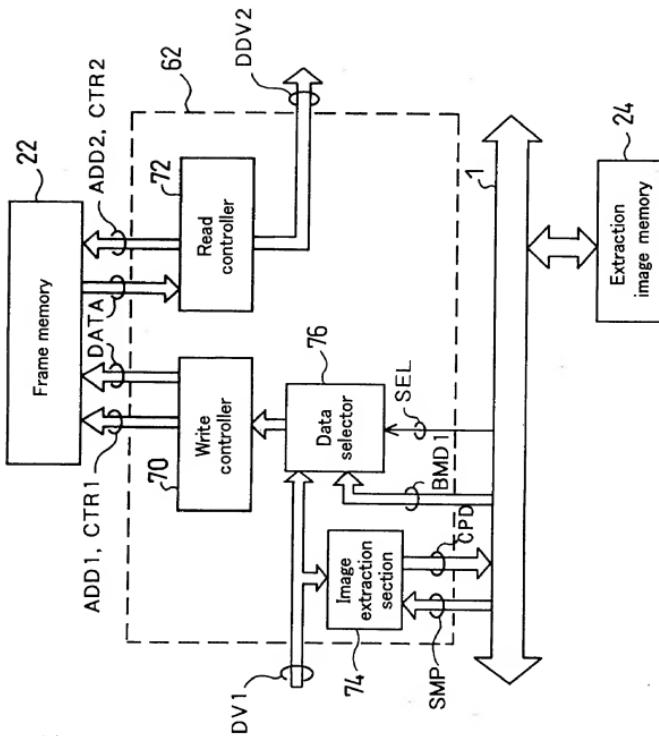
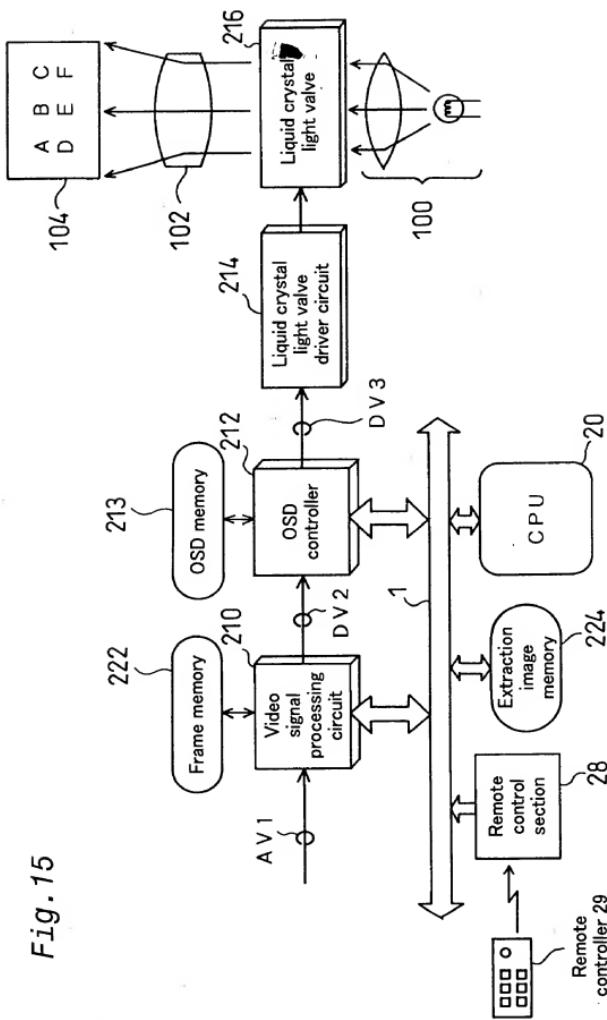


Fig. 15



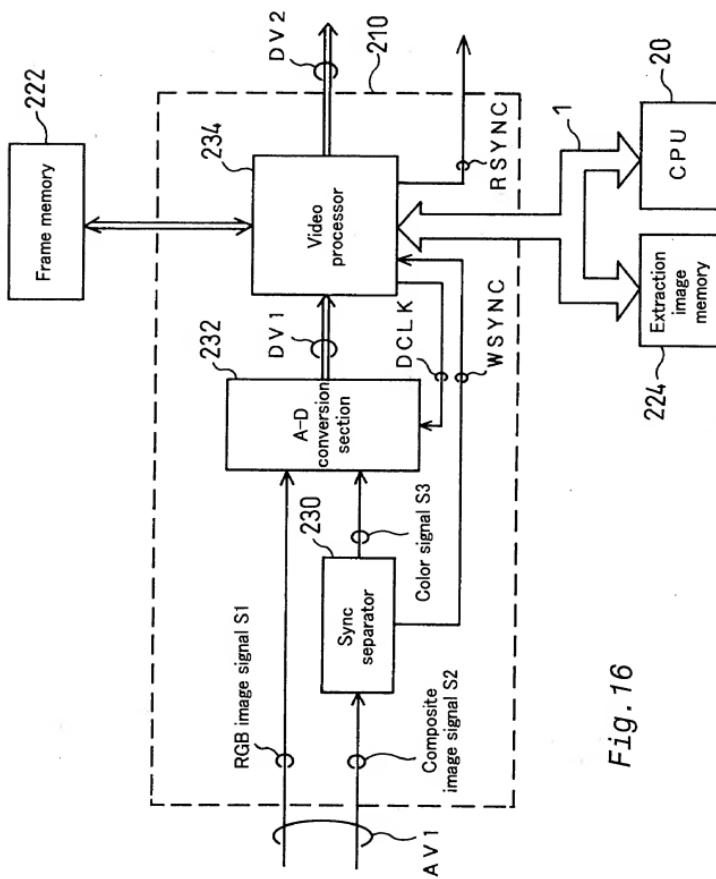


Fig. 16

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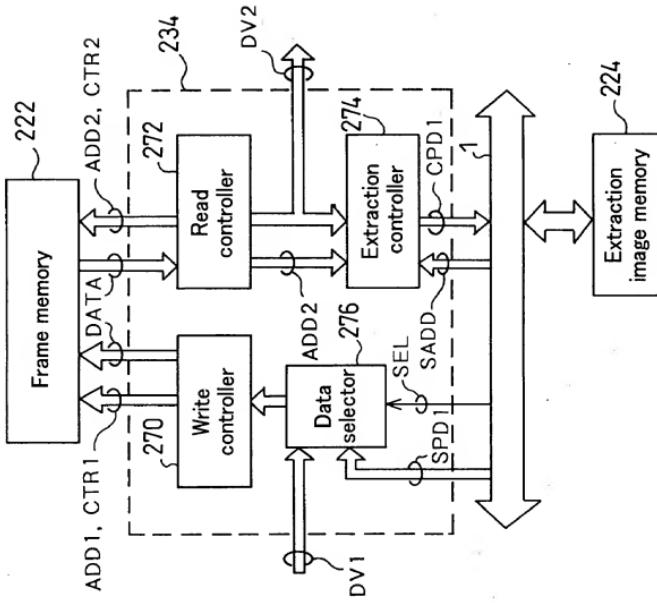
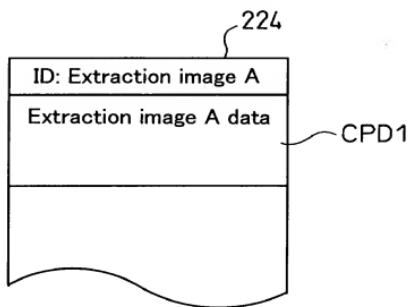


Fig. 17

Fig. 18



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Fig. 19

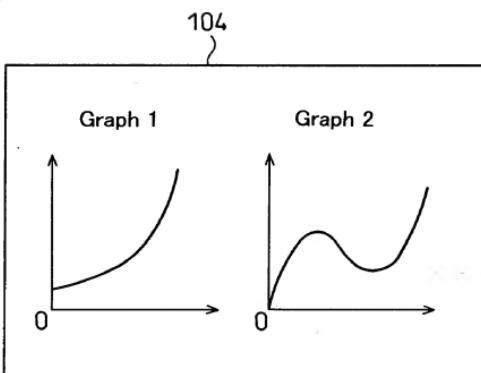


Fig. 20

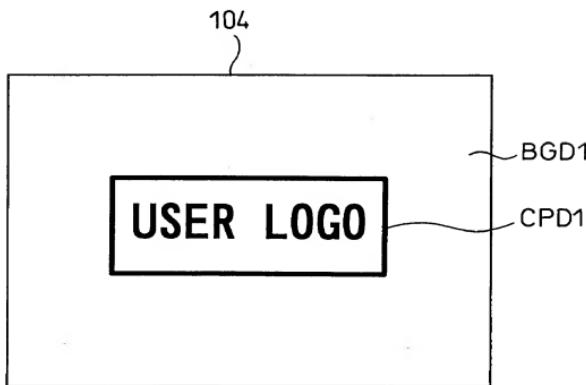


Fig. 21(a)

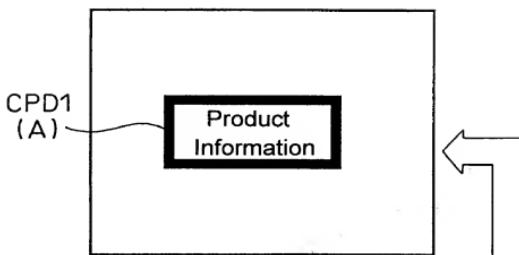


Fig. 21(b)

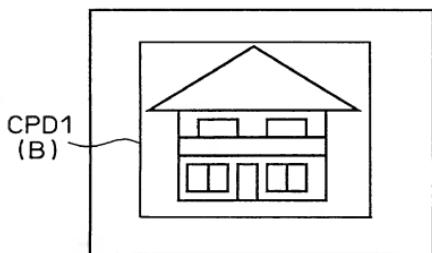
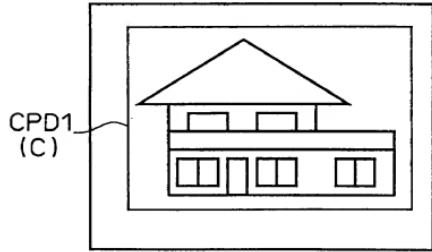


Fig. 21(c)



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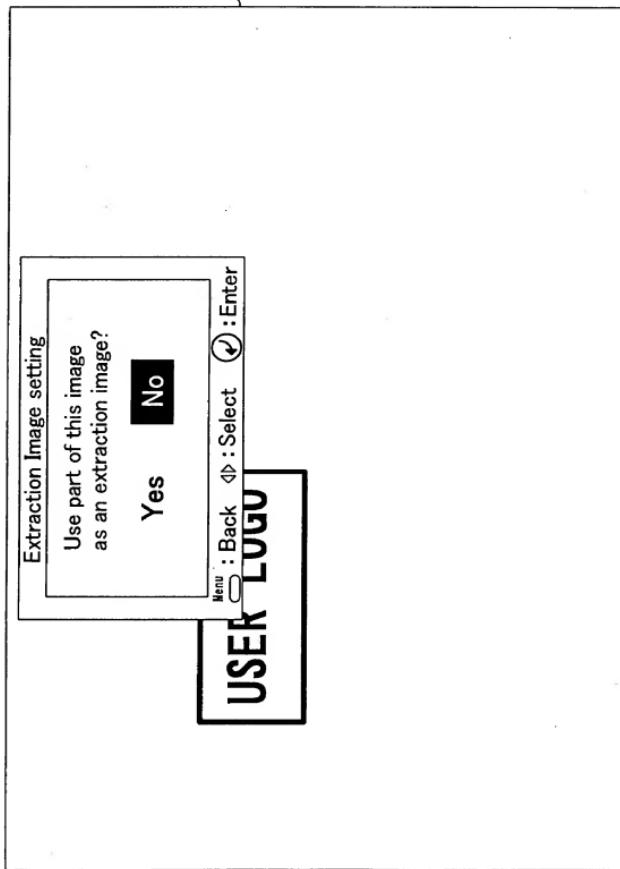


Fig.22

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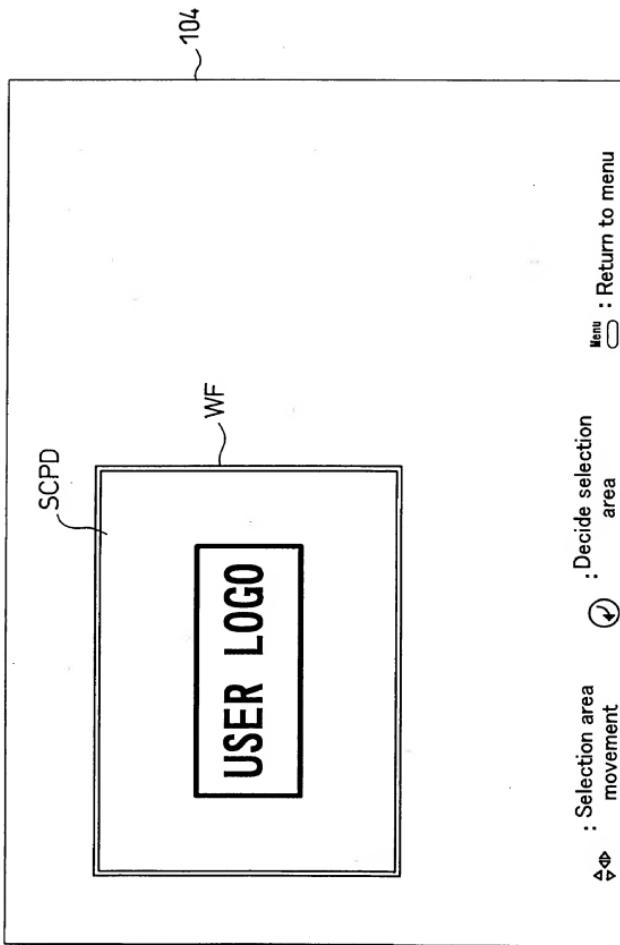


Fig. 23

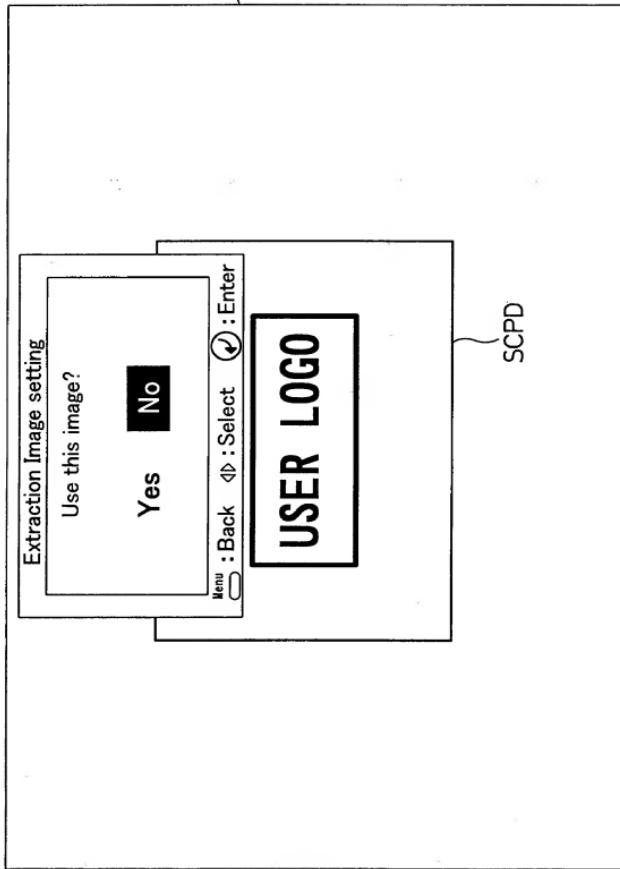


Fig.24

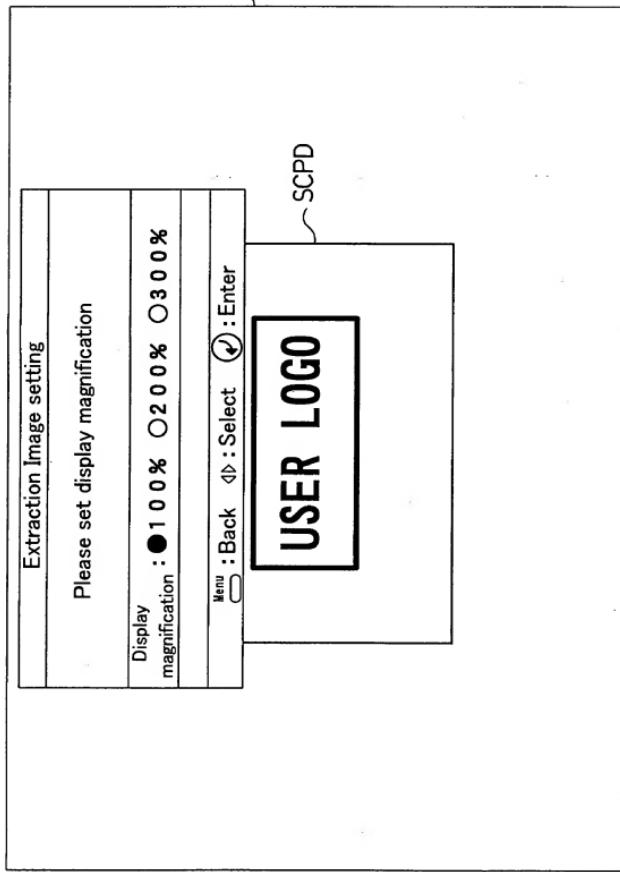


Fig. 25

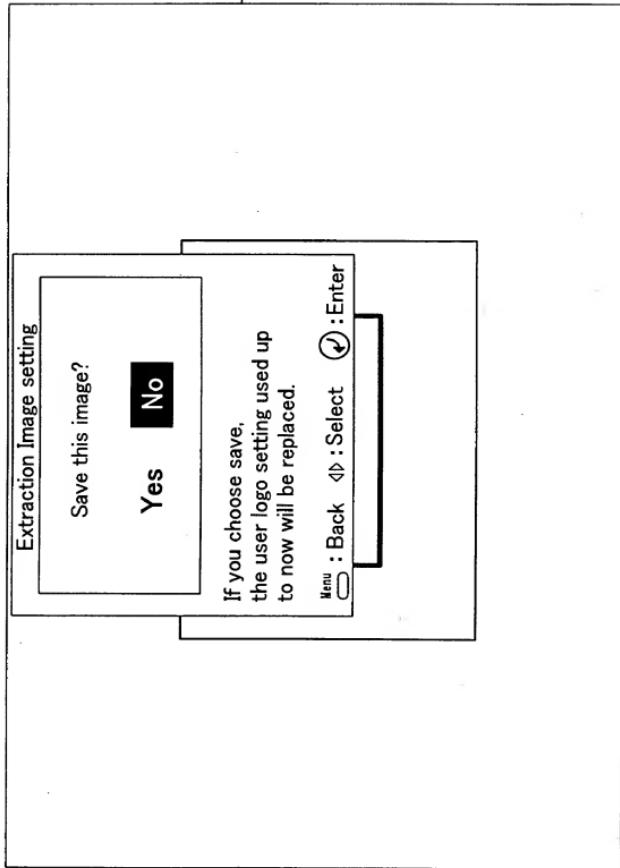


Fig. 26

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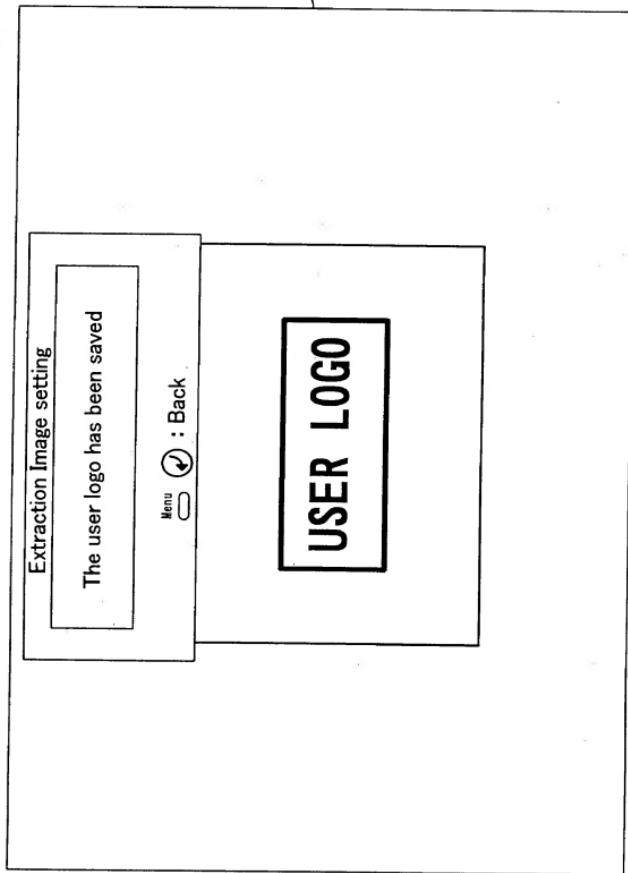


Fig.27

Fig. 28

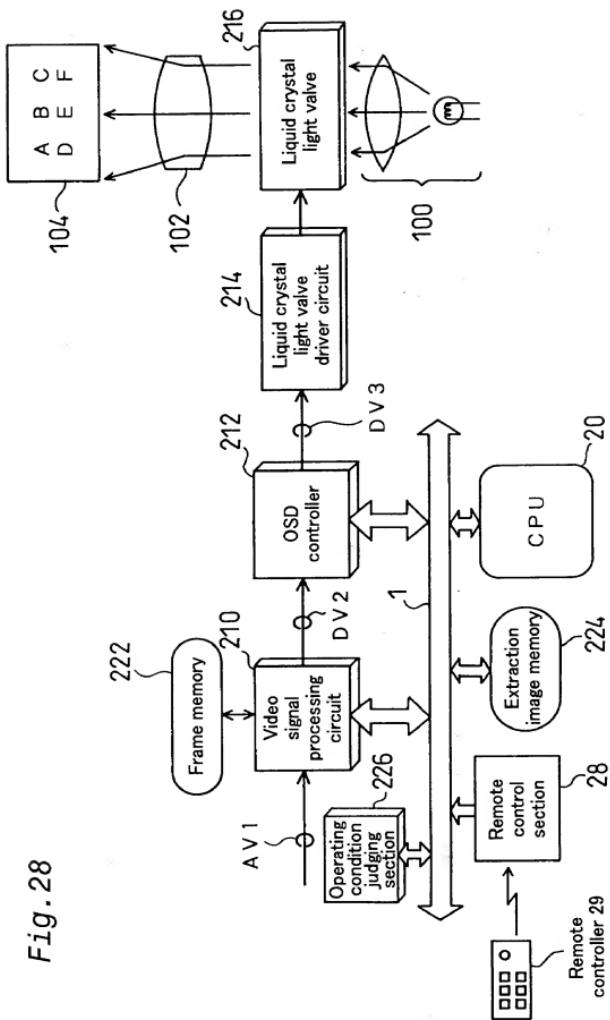


Fig. 29

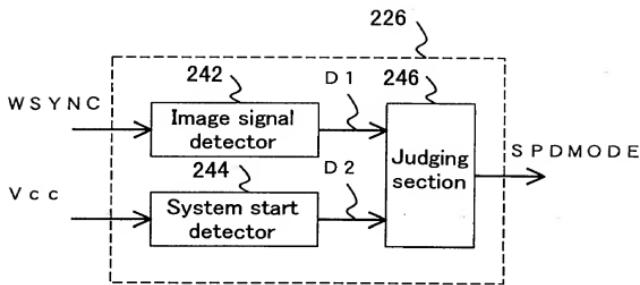


Fig. 30(a)

Vcc

Fig. 30(b)

D2

Fig. 30(c)

WSYNC

Fig. 30(d)

D1

Fig. 30(e)

SPD MODE

